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		Filing Date		2004-06-01	
		First Named Inventor		Graham Balsdon	
		Art Unit		2173	
		Examiner Name			
		Attorney Docket Number		21648-000700US	

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1	Ng, "Gridless' Variable-Width Channel Router for Macro Cell Design", 24th Conference on Design Automation, May 28 - June 1, 2987, pp. 633-636	<input type="checkbox"/>
2	Dubois et al., "New Advances in the Routing of Mixed Analog and Digital Channels", IEEE International Symposium on Circuits and Systems, Vol. 4, June 11-14, 1991, pp. 1956-1959	<input type="checkbox"/>
3	Polk, "A Three-Layer Gridless Channel Router with Compaction", 24th Conference on Design Automation, May 28 - June 1, 1987, pp. 146-151	<input type="checkbox"/>
4	Chen et al., "Glitter: A Gridless Variable-Width Channel Router", IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 5, No. 4, pp. 459 - 465	<input type="checkbox"/>

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- Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
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